

FIG. 1 - SYSTEM

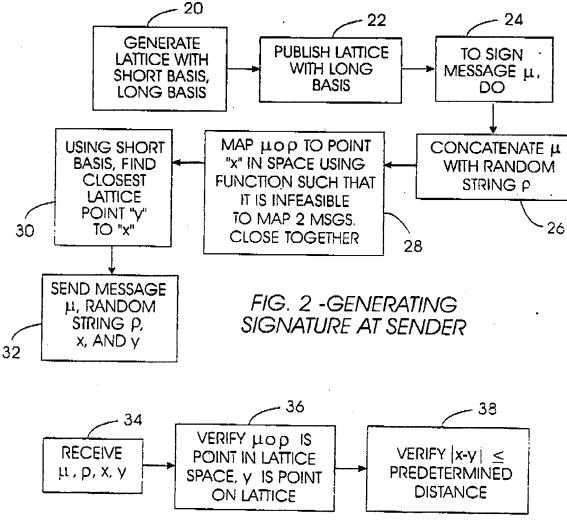


FIG. 3 - VERIFYING SIGNATURE AT RECEIVER